



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Grigg et. al.

Serial No.: 10/608,749

Filed: June 26, 2003

For: METHODS FOR LABELING
SEMICONDUCTOR DEVICE
COMPONENTS

Examiner: Unknown

Group Art Unit: 1732

Attorney Docket No.: 2269-4211.6US
(99-0218.06/US)

CERTIFICATE OF MAILING

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
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Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 6,251,488	06/26/01	Miller et al.
US - 6,259,962	07/10/01	Gothait
US - 6,268,584	07/31/01	Keicher et al.
US - 6,326,698	12/04/01	Akram
US - 6,391,251	05/21/02	Keicher et al.
US - 6,432,752	08/13/02	Farnworth
US - 6,482,576	11/19/02	Farnworth et al.
US - 6,514,798	02/04/03	Farnworth
US - 6,524,346	02/25/03	Farnworth
US - 6,544,821	04/08/03	Akram
US - 6,544,902	04/08/03	Farnworth
US - 6,549,821	04/15/03	Farnworth et al.
US - 6,562,278	05/13/03	Farnworth et al.
US - 6,593,171	07/15/03	Farnworth

Other Documents

U.S. Patent Application Publication No. 2002/0066966 A1 to Farnworth, dated June 6, 2002

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U.S. Patent Application Publication No. 2003/0089999 A1 to Akram, dated May 15, 2003

U.S. Patent Application Publication No. 2003/0092220 A1 to Akram, dated May 15, 2003

U.S. Patent Application Publication No. 2003/0093173 A1 to Farnworth et al., dated May 15, 2003

U.S. Patent Application Publication No. 2003/0102566 A1 to Farnworth, dated June 5, 2003

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MILLER et al., "Maskless Mesoscale Materials Deposition", Deposition Technology, September 2001, pages 20-22

MILLER, "New Laser-Directed Deposition Technology", Microelectronic Fabrication, August 2001, page 16

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Webpage, Objet Prototyping the Future, How it Works, 2 pages

U.S. Patent Application No. 09/589,841, filed June 8, 2000, entitled "Stereolithographic Methods for Forming a Protective Layer on a Semiconductor Device Substrate and Substrates Including Protective Layers So Formed", inventor Farnworth et al.

U.S. Patent Application No. 09/651,930, filed August 31, 2000, entitled "Semiconductor Device Including Leads in Communication with Contact Pads Thereof and a Stereolithographically Fabricated Package Substantially Encapsulating the Leads and Methods for Fabricating the Same", inventor Salman Akram

U.S. Patent Application No. 10/370,755, filed February 20, 2003, entitled "Chip Scale Package Structures and Method of Forming Conductive Bumps Thereon", inventor Warren M. Farnworth

U.S. Patent Application No. 10/619,918, filed July 15, 2003, entitled "Stereolithographic Methods for Fabricating Hermetic Semiconductor Device Packages and Semiconductor Devices Including Stereolithographically Fabricated Hermetic Packages", inventor Warren M. Farnworth

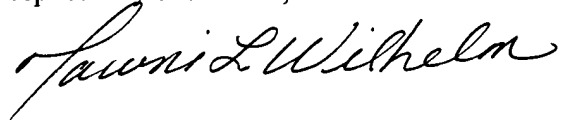
U.S. Patent Application No. 10/672,098, filed September 26, 2003, "Apparatus and Methods for Use in Stereolithographic Processing of Components and Assemblies", inventor Warren M. Farnworth

U.S. Patent Application No. 10/690,417, filed October 20, 2003, entitled "Methods of Coating and Singulating Wafers and Chip-Scale Packages Formed Therefrom", inventor Farnworth et al.

Applicants offer to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits.

Respectfully submitted,

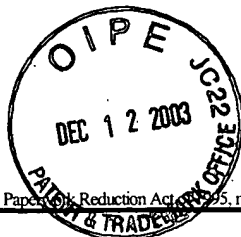


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Date: December 9, 2003
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STATEMENT BY APPLICANT**

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Application Number	10/608,749
Filing Date	June 26, 2003
First Named Inventor	Grigg et. al.
Group Art Unit	1732
Examiner Name	Unknown
Attorney Docket Number	4211.6US (99-0218.06/US)

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US- 6,251,488	06/26/01	Miller et al.	
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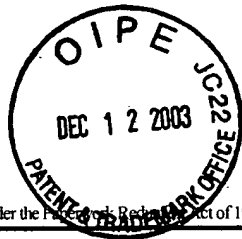
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		U.S. Patent Application Publication No. 2002/0066966 A1 to Farnworth, dated June 6, 2002	
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Examiner Name	Unknown
Attorney Docket Number	4211.6LIS (99-0218.06/LIS)

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		MILLER et al., "Maskless Mesoscale Materials Deposition", Deposition Technology, September 2001, pages 20-22	
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

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